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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10067704	FILING DATE 02/04/2002	CLASS 700	SUBCLASS 972	GAU 2121	EXAMINER PALADIN I
**APPLICANTS: Tuszynski Steve; 2125					
**CONTINUING DATA VERIFIED: <i>none</i> <i>au</i>					
** FOREIGN APPLICATIONS VERIFIED: <i>au</i>					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input checked="" type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		ATTORNEY DOCKET NO	
Verified and Acknowledged Examiners's initials <i>au</i>				6550/53675	
TITLE : Manufacturing design and process analysis system					

U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg. Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner	
		PREPARED FOR ISSUE	
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